

UNITED STATES PATENT AND TRADEMARK OFFICE



DATE MAILED: 08/06/2003

UNITED STATES DEPARTMENT OF COMMERCE
United States Patent and Trademark Office
Address: COMMISSIONER FOR PATENTS
P.O. Box 1450
Alexandria, Virginia 22313-1450
www.uspto.gov

APPLICATION NO.	FI	ILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/938,106 08/2		8/23/2001 James M. Derderian		4832US (01-0104)	1038
24247	7590	08/06/2003			
TRASK BI	RITT		EXAMINER		
P.O. BOX 2550 SALT LAKE CITY, UT 84110				IM, JUNGHWA M	
				ART UNIT	PAPER NUMBER
				2811	· -

Please find below and/or attached an Office communication concerning this application or proceeding.

	Application No.	Applicant(s)
~	Application No.	DERDERIAN, JAMES M.
Advisory Action	09/938,106 Examiner	Art Unit
	Junghwa M. Im	2811
The MAILING DATE of this communication	"	
	APPLICATION IN CONDIT	TION FOR ALLOWANCE.
inal rejection under 37 CFR 1.113 may <u>only</u> be eith condition for allowance; (2) a timely filed Notice of A Examination (RCE) in compliance with 37 CFR 1.11	er: (1) a timely filed amend Appeal (with appeal fee); or	ment which places the application in
	R REPLY [check either a) o	or b)]
a) The period for reply expiresmonths from the m	ailing date of the final rejection.	ur II to the Such acceptant which ever in letter. In po
b) The period for reply expires on: (1) the mailing date of the event, however, will the statutory period for reply expire I ONLY CHECK THIS BOX WHEN THE FIRST REPLY 706.07(f).	ater than SIX MONTHS from the m WAS FILED WITHIN TWO MONT	alling date of the final rejection. THS OF THE FINAL REJECTION. See MPEP
Extensions of time may be obtained under 37 CFR 1.136(a). Extensions of time may be obtained under 37 CFR 1.136(a). The period of the second	f extension and the corresponding a ortened statutory period for reply orio	mount of the fee. The appropriate extension ree under sinally set in the final Office action; or (2) as set forth in
1. A Notice of Appeal was filed on Appearance 37 CFR 1.192(a), or any extension thereof (3	ellant's Brief must be filed w 67 CFR 1.191(d)), to avoid o	ithin the period set forth in Iismissal of the appeal.
2. The proposed amendment(s) will not be ente		•
(a) X they raise new issues that would require		r search (see NOTE below);
(b) they raise the issue of new matter (see !	Note below);	
issues for appeal; and/or		eal by materially reducing or simplifying the
(d) they present additional claims without of	anceling a corresponding n	umber of finally rejected claims.
NOTE: See Continuation Sheet.		
3. Applicant's reply has overcome the following	rejection(s):	w
4. Newly proposed or amended claim(s) canceling the non-allowable claim(s).		
5. ☐ The a) ☐ affidavit, b) ☐ exhibit, or c) ☐ required application in condition for allowance becau	se:	
6. The affidavit or exhibit will NOT be consider raised by the Examiner in the final rejection		
7. For purposes of Appeal, the proposed amen explanation of how the new or amended cla	dment(s) a)⊠ will not be er ims would be rejected is pro	ntered or b)⊡ will be entered and an ovided below or appended.
The status of the claim(s) is (or will be) as for	ollows:	
Claim(s) allowed:		
Claim(s) objected to:		
Claim(s) rejected: 23-27,29-35,40-51 and 53-	<u>64</u> .	
Claim(s) withdrawn from consideration:	·	
a Commission filed on	ic a) approved or b)□ disapproved by the Examiner.
8. The proposed drawing correction filed on9. Note the attached Information Disclosure St10. Other:	atement(s)(PTO-1449) Par	per No(s)
10. Other:		elow pouns
		TOM THOMAS
		SUPERVISORY PATENT EXAMINER

TECHNOLOGY CENTER 2800

Continuation Sheet (PTO-303) 09/938,106





Application No.

Continuation of 2. NOTE: The amended portion of claims 23 and 45 with the proposed limitation of 'a back side of the second semiconductor device(or the second semiconductor device) being stably supported by the discrete elements' is an example of new issue which would require further consideration and/or search to determine patentability.